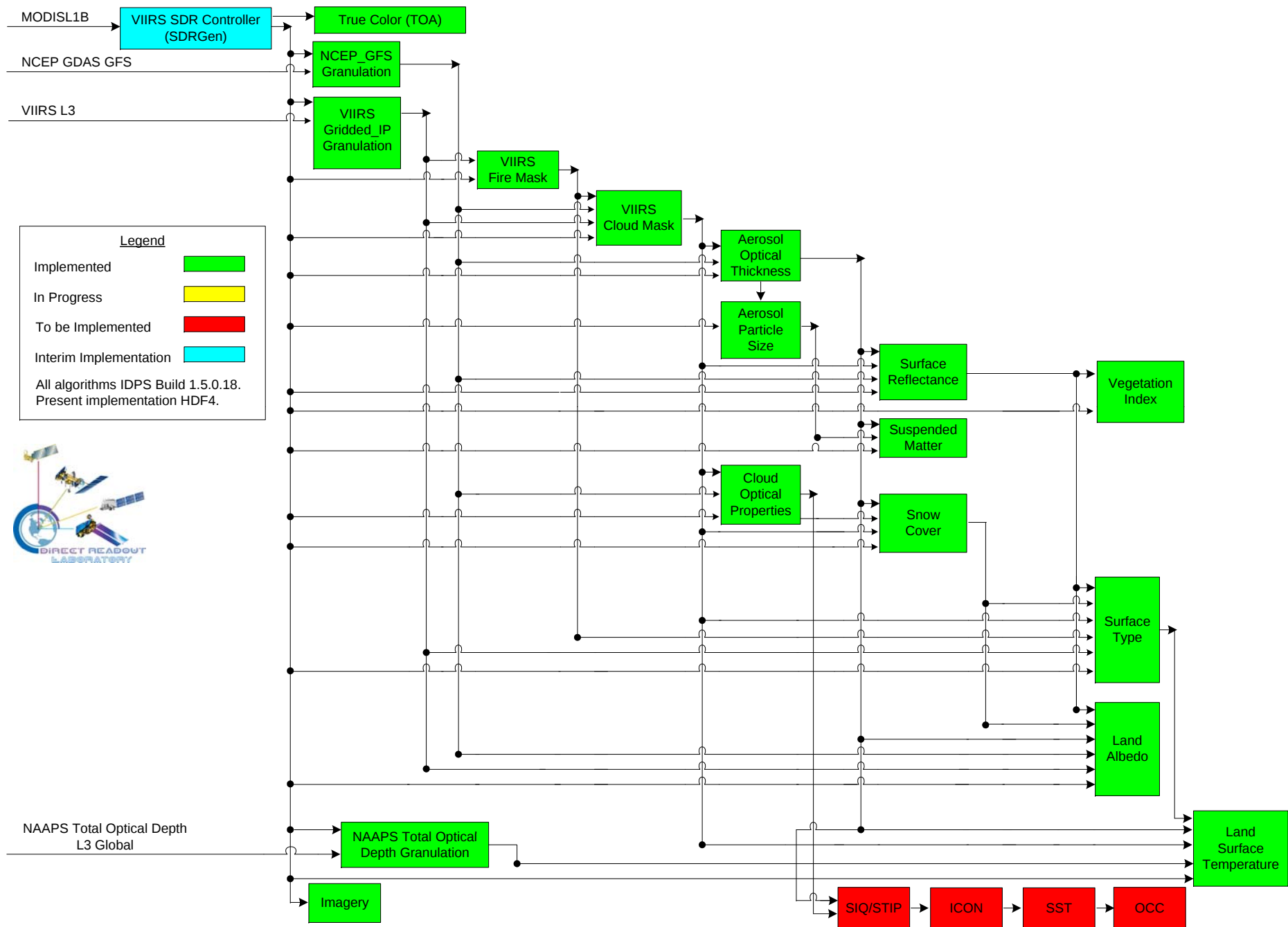




Phase 1 Direct Readout Implementation of the VIIRS Chained-processing Approach