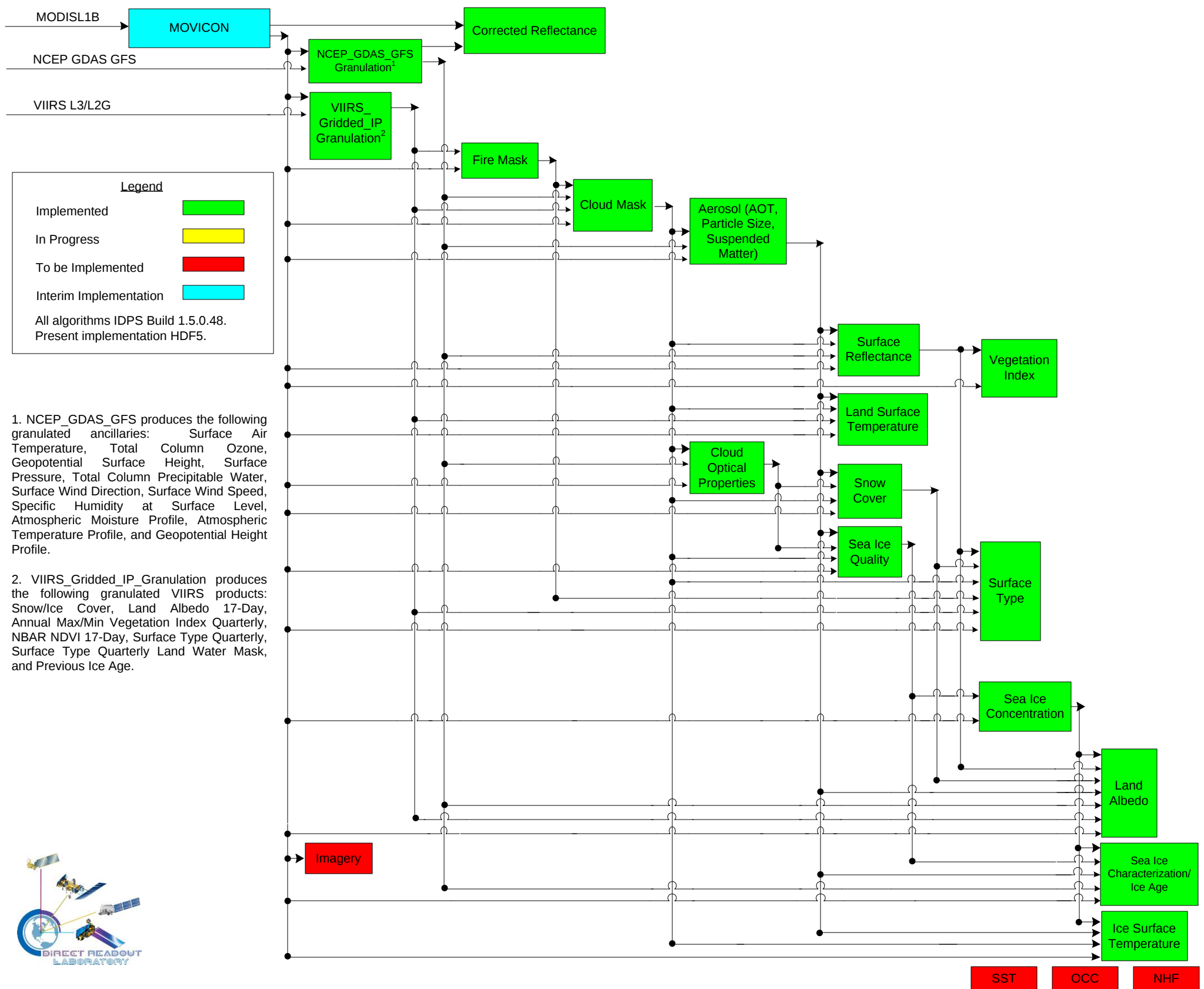




Phase 2 Direct Readout Implementation of the VIIRS Chained-processing Approach